



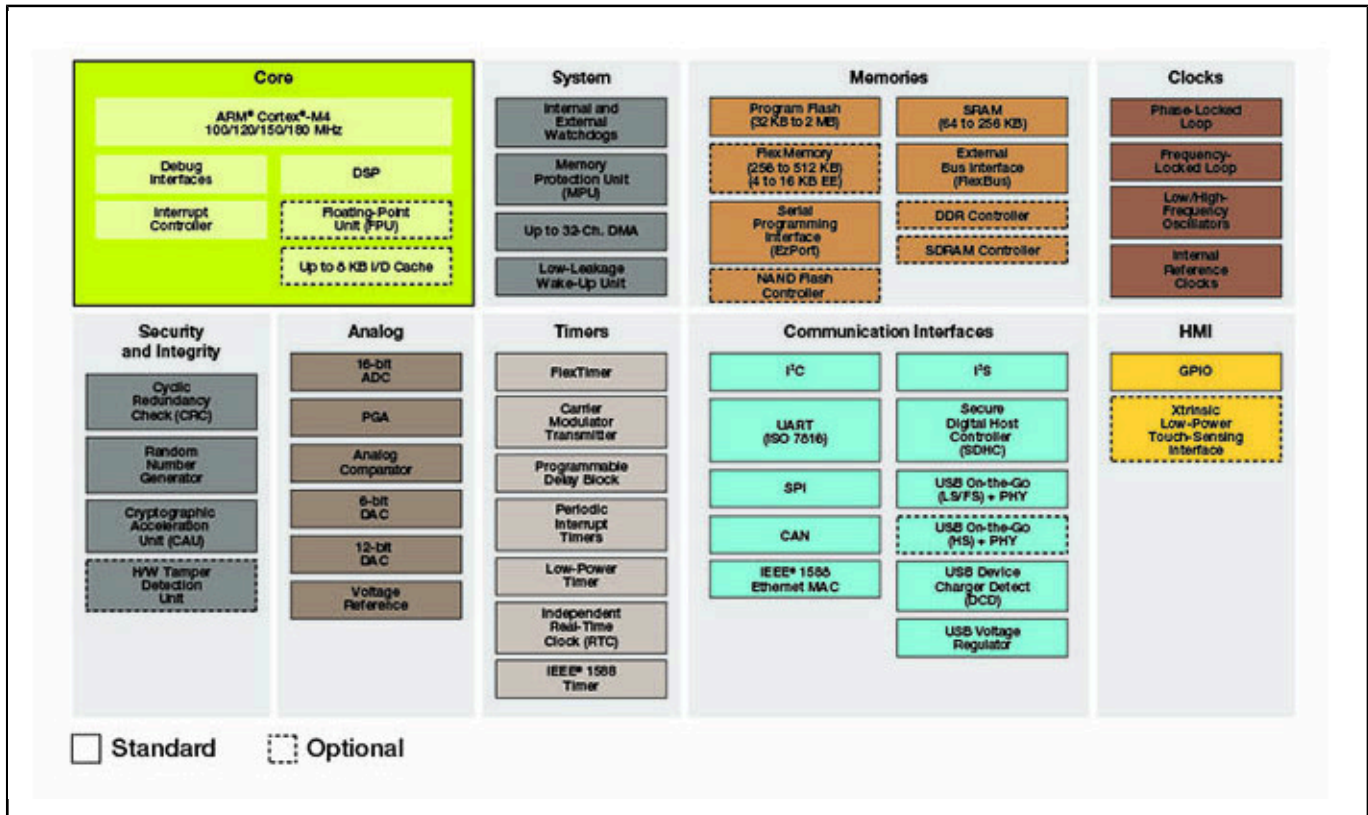
# **Kinetis® K60-100 MHz, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®-M4 Core**

## **K60\_100**

Last Updated: Jul 31, 2023

The Kinetis® K60 100 MHz IEEE® 1588 Ethernet MCUs are built on the Arm® Cortex® -M4 core and feature advanced analog integration and serial communication. Available down to a 5 mm x 5 mm wafer-level chip-scale package (WLCSP), these devices maximize board space and enhance performance with minimum-length interconnections, allowing the miniaturization of existing applications. This family shares the comprehensive enablement and scalability of the Kinetis portfolio.

# Kinetis K6x MCU Family Block Diagram Block Diagram



View additional information for [Kinetis® K60-100 MHz, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®-M4 Core](#).

Note: The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.