



Dual Low-Ohmic Single-Pole Single-Throw Analog Switch

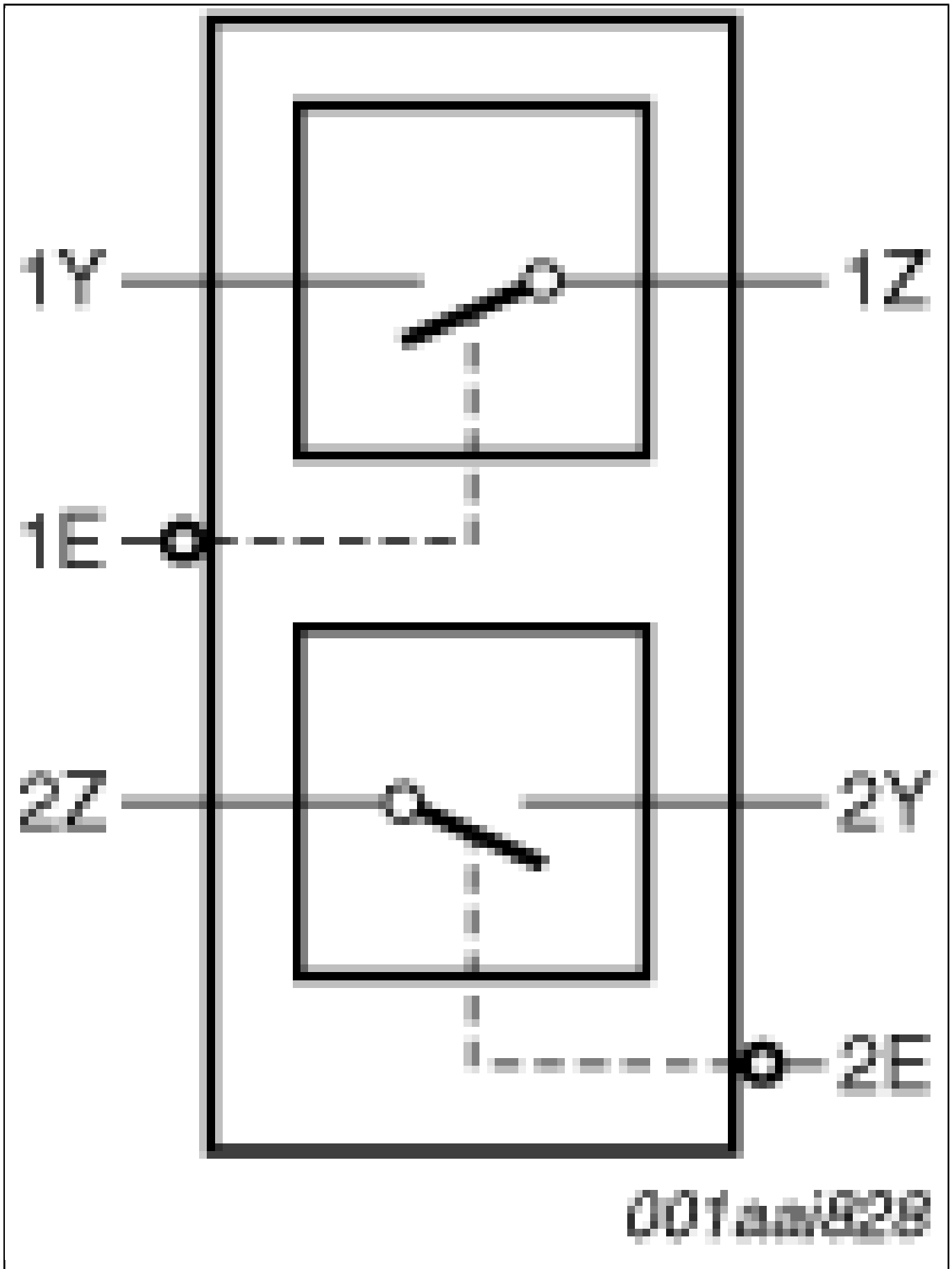
NX3L2G384

Last Updated: Jan 31, 2024

The NX3L2G384 is a dual low-ohmic single-pole single throw analog switch. Each switch has two input/output terminals (nY and nZ) and an active LOW enable input (nE). When pin nE is HIGH, the analog switch is turned off.

Schmitt trigger action at the enable input (nE) makes the circuit tolerant to slower input rise and fall times. The NX3L2G384 allows signals with amplitude up to VCC to be transmitted from nY to nZ; or from nZ to nY. Its low ON resistance (0.5 Ω) and flatness (0.13 Ω) ensures minimal attenuation and distortion of transmitted signals.

Block diagram: NX3L2G384GD, NX3L2G384GM, NX3L2G384GT Block Diagram



View additional information for [Dual Low-Ohmic Single-Pole Single-Throw Analog Switch](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.