



12-Channel Configurable PMIC for i.MX6 and i.MX7 Application Processors

PF3000

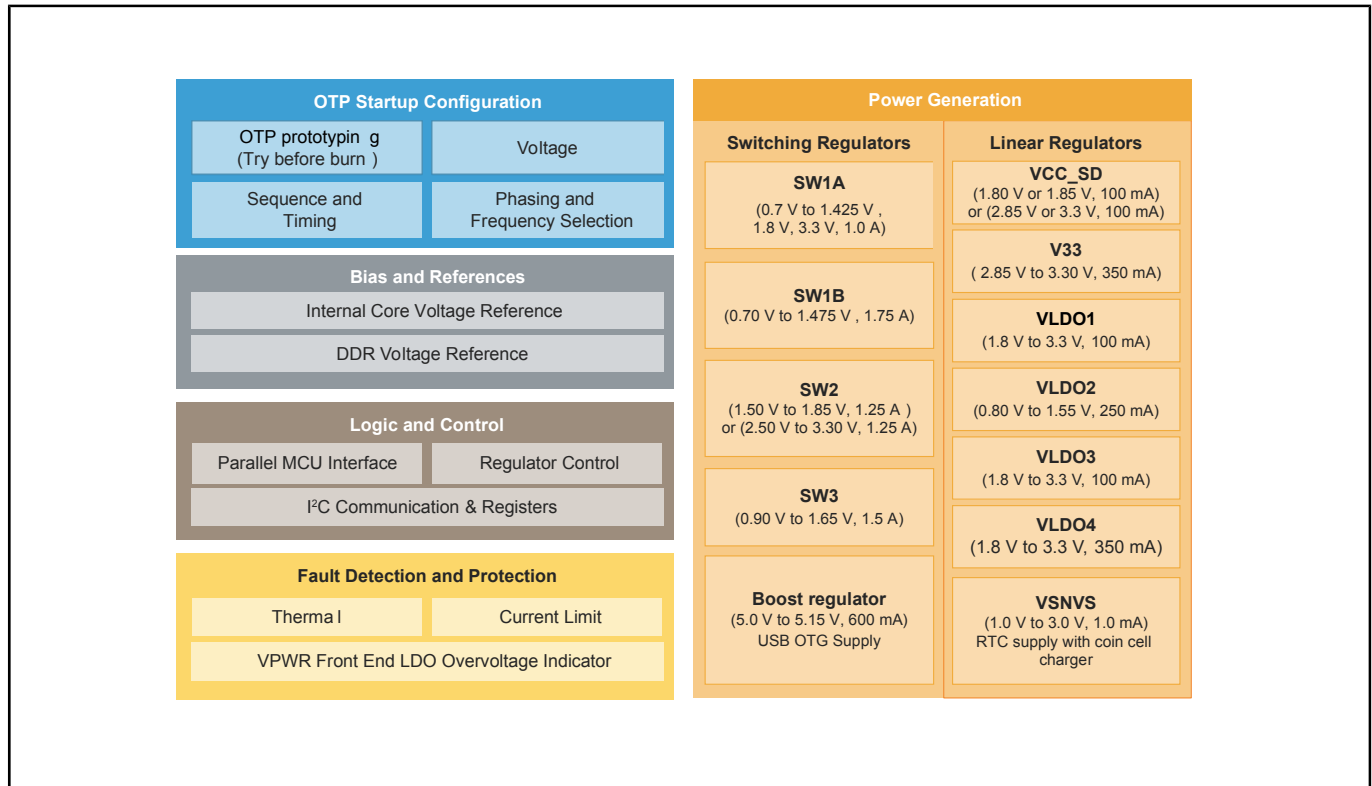
Last Updated: May 25, 2023

The PF3000 power management integrated circuit (PMIC) features a configurable architecture that supports numerous outputs with various current ratings as well as programmable voltage and sequencing. This enables the PF3000 to power the core processor, external memory and peripherals to provide a single-chip system power solution.

The PF3000 is ideally suited to Cortex® A7 based i.MX 7Solo and i.MX 7Dual application processors to meet low power application requirements. The PF3000 is a great companion for the very low power Cortex® A9 core product family including the i.MX 6DualLite and all single-core Cortex A9 i.MX processors.

Compatibility with i.MX applications processors are shown in multiple reference designs and facilitates software controlled, dynamic voltage scaling.

PF3000 Block Diagram



View additional information for [12-Channel Configurable PMIC for i.MX6 and i.MX7 Application Processors](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.