



# Enhanced Performance HDMI/DVI Level Shifter with Active DDC Buffer, Supporting 1.65 Gbit/s Operation

## PTN3361CBS

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PTN3361C is a high-speed level shifter device which converts four lanes of low-swing AC-coupled differential input signals to DVI v1.0 and HDMI v1.4b compliant open-drain current-steering differential output signals, up to 1.65 Gbit/s per lane to support 1080p applications. Each of these lanes provides a level-shifting differential buffer to translate from low-swing AC-coupled differential signaling on the source side, to TMDS-type DC-coupled differential current-mode signaling terminated into 50 Ohms to 3.3 V on the sink side. Additionally, PTN3361C provides a single-ended active buffer for voltage translation of the HPD signal from 5 V on the sink side to 3.3 V on the source side and provides a channel with active buffering and level shifting of the DDC channel (consisting of a clock and a data line) between 3.3 V source-side and 5 V sink-side. The DDC channel is implemented using active I<sup>2</sup>C-bus buffer technology providing capacitive isolation, redriving and level shifting as well as disablement (isolation between source and sink) of the clock and data lines.

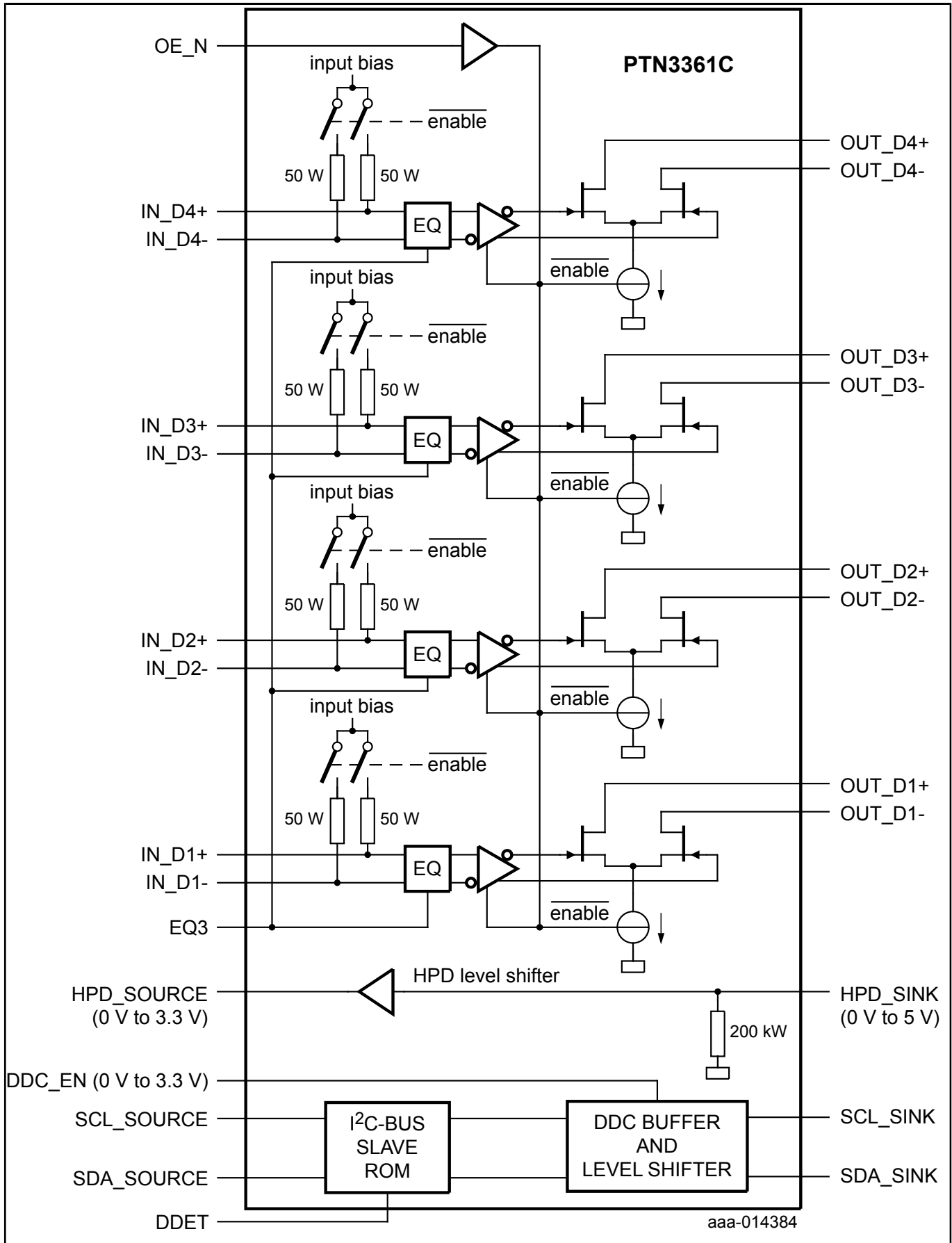
The low-swing AC-coupled differential input signals to PTN3361C typically come from a display source with multi-mode I/O, which supports multiple display standards, for example, DisplayPort, HDMI and DVI. While the input differential signals are configured to carry DVI or HDMI coded data, they do not comply with the electrical requirements of the DVI v1.0 or HDMI v1.4b specification. By using PTN3361C, chip set vendors are able to implement such reconfigurable I/Os on multi-mode display source devices, allowing the support of multiple display standards while keeping the number of chip set I/O pins low.

PTN3361C features low-swing self-biasing differential inputs which are compliant to the electrical specifications of DisplayPort Standard v1.2 and/or PCI Express Standard v1.1, and open-drain current-steering differential outputs compliant to DVI v1.0 and HDMI v1.4b electrical specifications. The I<sup>2</sup>C-bus channel actively buffers as well as level-translates the DDC signals for optimal capacitive isolation. PTN3361C also supports power-saving modes in order to minimize current consumption when no display is active or connected.

PTN3361C can be used for either HDMI or DVI level shifting. It provides additional features supporting HDMI dongle detection; since support of HDMI dongle detection via the DDC channel is mandatory, the system applications shall enable this feature for correct operation.

PTN3361C is powered from a single 3.3 V power supply and is offered in a 32-terminal HVQFN32 package.

# PTN3361C Block Diagram Block Diagram



View additional information for [Enhanced Performance HDMI/DVI Level Shifter with Active DDC Buffer, Supporting 1.65 Gbit/s Operation](#).

**Note:** The information on this document is subject to change without notice.

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