



# Mini High-Speed CAN System Basis Chip

## UJA1169ATK

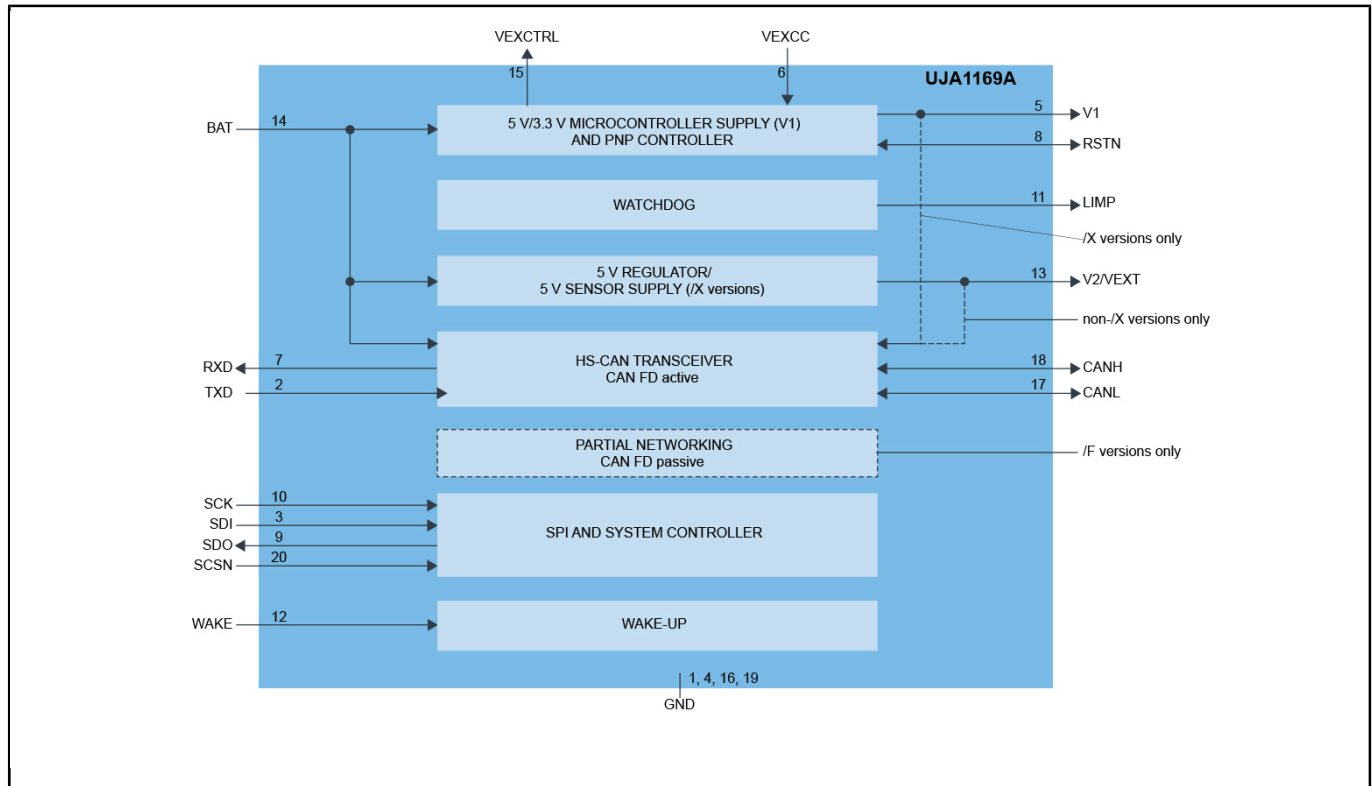
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The UJA1169ATK is a mini high-speed CAN system basis chip (SBC) family containing an ISO 11898-2:2016 and SAE J2284-1 to SAE J2284-5 compliant HS-CAN transceiver including CAN FD up to 5 Mbit/s and an integrated 5 V or 3.3 V, 250 mA supply. It also features a watchdog, a serial peripheral interface (SPI) and LIMP output.

The UJA1169ATK comes in six variants:

- The UJA1169ATK, UJA1169ATK/F, UJA1169ATK/X and UJA1169ATK/X/F contain a 5 V regulator (V1). V1 is a 3.3 V regulator in the UJA1169ATK/3 and the UJA1169ATK/F/3.
- The UJA1169ATK/X and UJA1169ATK/X/F are equipped with a 5 V supply (VEXT) for off-board loads.
- The UJA1169Axx/F variants support ISO 11898-2:2016 compliant CAN partial networking with a selective wake-up function incorporating CAN FD-passive.

# UJA1169ATK Block Diagram Block Diagram



View additional information for [Mini High-Speed CAN System Basis Chip](#).

**Note:** The information on this document is subject to change without notice.

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