



# Enhanced Time Processor Unit

## eTPU

Last Updated: Dec 22, 2022

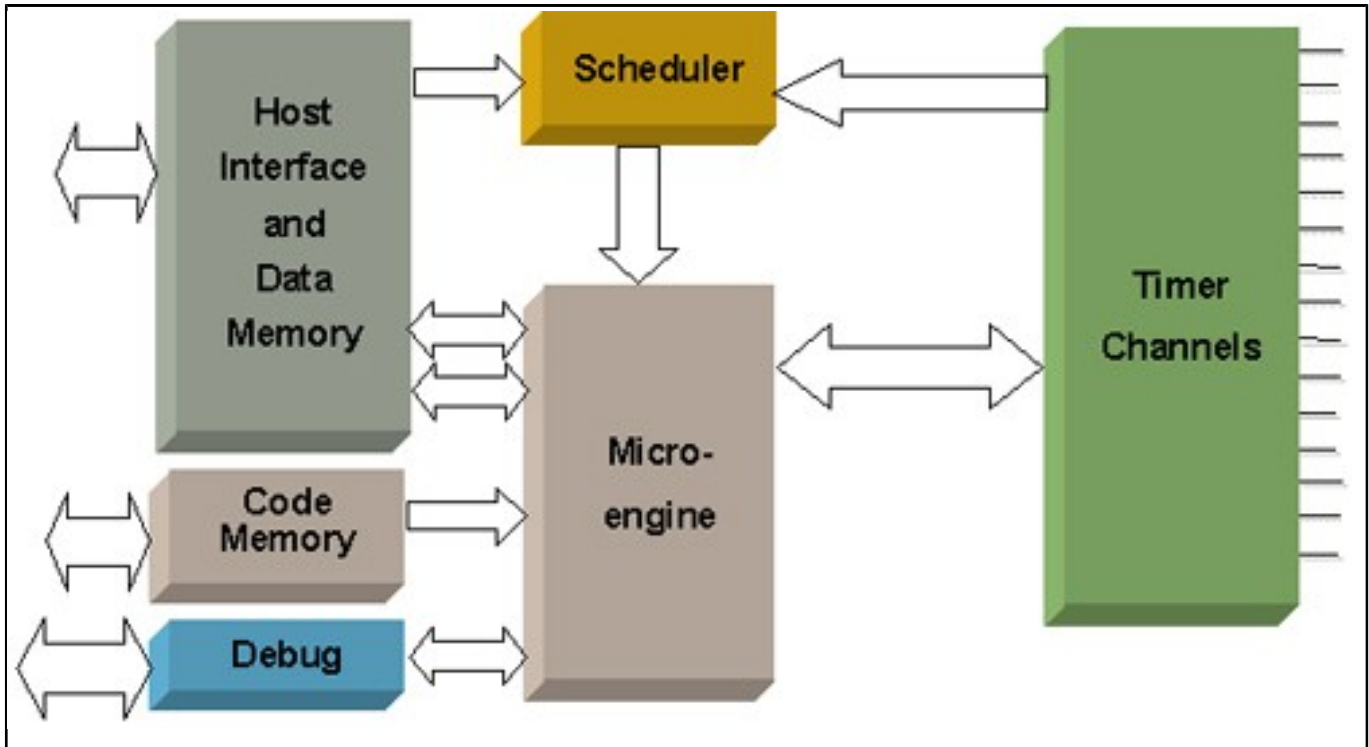
Visit the new CW eTPU Function Selector to create and download your eTPU function set based on new eTPU library functions and CodeWarrior® compiler.

Visit the eTPU Function Selector to download your eTPU functions from original set1, set2, set3 and set4.

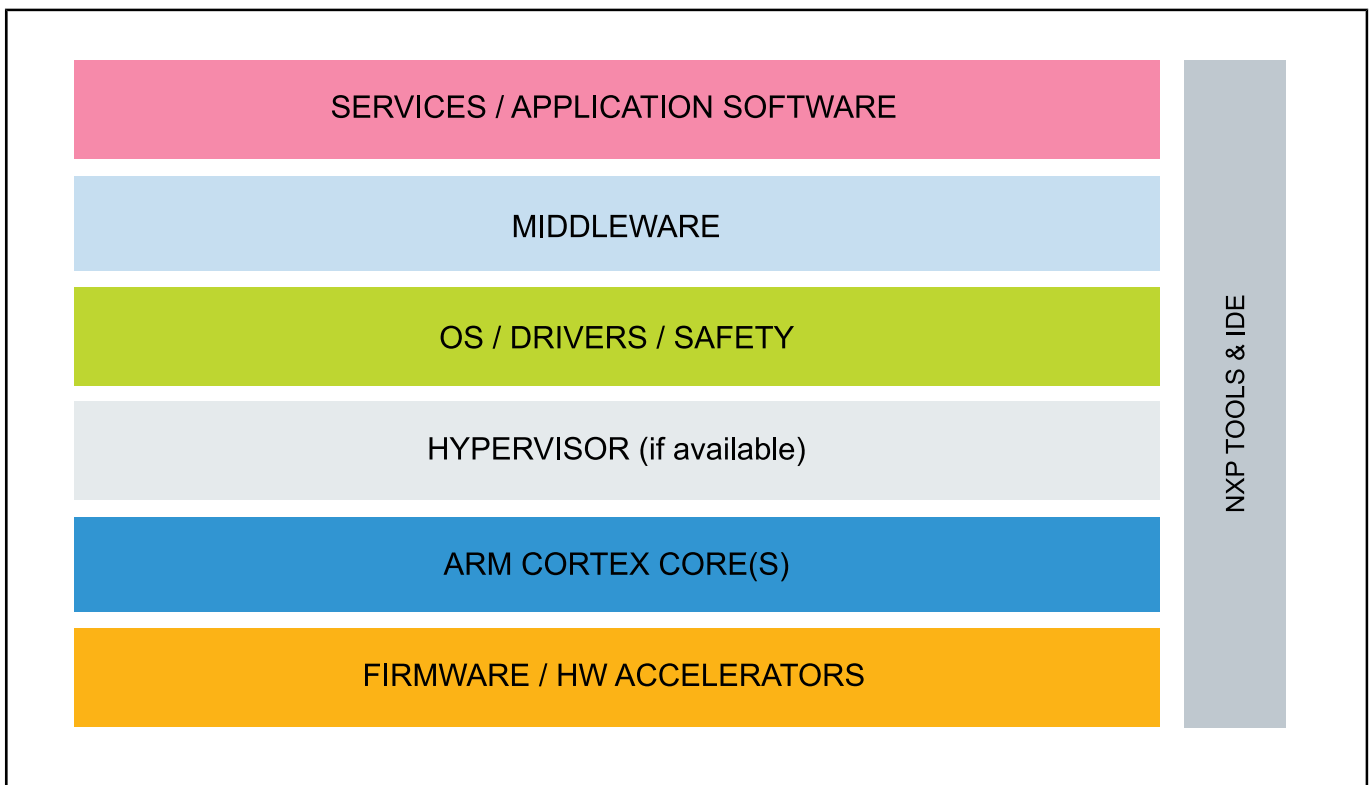
The NXP® eTPU is a programmable I/O controller with its own core and memory system, allowing it to perform complex timing and I/O management independently of the CPU.

- The eTPU is essentially an independent MCU designed for timing control, I/O handling, serial communications, motor and engine control applications
- Enhancements include a more powerful processor, which handles high-level C code efficiently and allows for more functionality and increased performance
- NXP provides an eTPU functions library that is a superset of the standard TPU library functions; these, along with an available C compiler, make it easy to port older applications to the eTPU

### Enhanced Time Processor Unit Block Diagram



### Automotive General Block Diagram Block Diagram



View additional information for [Enhanced Time Processor Unit](#).

**Note:** The information on this document is subject to change without notice.

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