

MPC5676R Mask Identification Codes

Contents

1	Introduction.....	1
2	MCU ID Codes.....	1
3	JTAG Device ID.....	2

1 Introduction

This engineering bulletin contains the MCU ID Register (MIDR) codes and JTAG ID codes for all existing revisions of Freescale's MPC5676R 32-bit dual-core MCU. Device revisions are referred to by revision number or silicon mask identifier. Both of these are given in this document for easy reference. This document presents both for easy reference.

2 MCU ID Codes

The following table shows the System Integration Unit's MCU ID Register (SIU_MIDR) codes for major and minor mask number as well as the external revision number associated with each silicon mask revision. The silicon mask revision code is marked on the top of each chip package below the part number.

Table 1. MIDR and Revision Numbers

Mask	MASKNUM_ MAJOR	MASKNUM_ MINOR	Revision Number
1N23A	0	0	1.1
2N23A	0	1	1.2
3N23A	0	1	1.3

3 JTAG Device ID

The JTAG device identification register allows the part revision number, design center, part identification number, and manufacturer identity code to be determined through the debug interface. The part revision number (PRN) may change for a major mask revision. The MPC5676R has not yet had a major revision, therefore, this number remains constant at the default value shown in the reference manual of the device.

The MPC5676R's JTAG DID is 0x0827_601D for all current silicon mask revisions.

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Web Support:

<http://www.freescale.com/support>

USA/Europe or Locations Not Listed:

Freescale Semiconductor
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
+1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor China Ltd.
Exchange Building 23F
No. 118 Jianguo Road
Chaoyang District
Beijing 100022
China
+86 10 5879 8000
support.asia@freescale.com

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