

56F8357

Target Applications

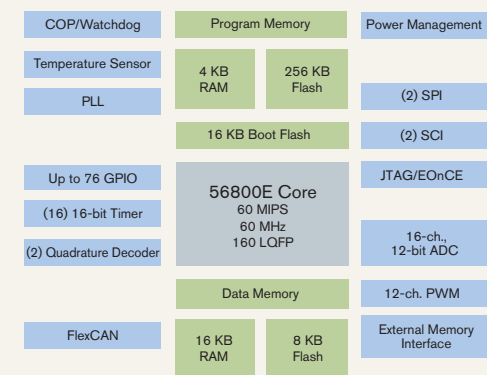
- > Automotive control
- > Industrial control/connectivity
- > Advanced motion control
- > Home appliances
- > General-purpose inverters
- > Smart relays
- > Fire and security systems
- > Power management
- > Medical monitoring
- > Multiphase inverters

Overview

Having met the memory needs of your application, do you long for more general-purpose input/output (GPIO) pins? Wishing for a handful of additional GPIO, without having to pay for additional memory you don't need? Enter the 56F8357, which offers identical memory and peripheral capabilities as the 56F8356, but with 14 additional GPIO pins.

If the challenge you face is not one of functionality but one of memory, the 56F8357 has you covered. The 56F8357 comes in the same package and package pin-out as the 56F8347. You keep the same functionality, while doubling your program memory. How's that for a migration strategy?

The 56F8357 is further proof that the 56F8300 Series is the right choice to overcome all of your design challenges.



56800E Core Features

- > Up to 60 MIPS at 60 MHz execution frequency
- > DSP and microcontroller (MCU) functionality in a unified, C-efficient architecture
- > JTAG/enhanced on-chip emulation (EOnCE™) for unobtrusive, real-time debugging
- > Four 36-bit accumulators
- > 16- and 32-bit bidirectional barrel shifter
- > Parallel instruction set with unique addressing modes
- > Hardware DO and REP loops available
- > Three internal address buses
- > Four internal data buses
- > Architectural support for 8-, 16- and 32-bit single-cycle data fetches
- > MCU-style software stack support
- > Controller-style addressing modes and instructions
- > Single-cycle 16 x 16-bit parallel multiplier-accumulator (MAC)
- > Proven to deliver more control functionality with a smaller memory footprint than competing architectures

Benefits

- > Hybrid architecture facilitates implementation of both control and signal processing functions in a single device
- > High-performance, secured Flash memory helps eliminate the need for external storage devices
- > Extended temperature range allows for operation of nonvolatile memory in harsh environments
- > Flash memory emulation of EEPROM helps eliminate the need for external nonvolatile memory
- > 32-bit performance with 16-bit code density
- > On-chip voltage regulator and power management help reduce overall system cost
- > Off-chip memory expansion capabilities allow for glueless interfacing with the additional memory of external devices without sacrificing performance
- > This device boots directly from Flash, providing additional application flexibility
- > High-performance pulse-width modulation (PWM) with programmable fault capability helps to simplify design and promotes compliance with safety regulations
- > PWM and analog-to-digital converter (ADC) modules are tightly coupled to help reduce processing overhead
- > Low-voltage interrupts (LVIs) help protect the system from brownout or power failure
- > GPIO pins support application-specific needs
- > Simple in-application Flash memory programming via EOnCE or serial communication

56F8357 Memory Features

- > Architecture permits as many as three simultaneous accesses to program and data memory
- > On-chip memory includes high-speed volatile and nonvolatile components:
 - 280 KB total Flash memory
 - › 256 KB of Program Flash
 - › 8 KB of Data Flash
 - › 16 KB of Boot Flash
 - 4 KB of Program RAM
 - 16 KB of Data RAM
- > Memories operate at 60 MHz (zero wait states) over temperature range (-40°C to +125°C) with no software tricks or hardware accelerators required
- > Flash security feature helps prevent unauthorized accesses to its content
- > Off-chip memory expansion capabilities provide a simple method for interfacing additional external memory and/or peripheral devices
 - Access up to 4 MB of external program memory or 32 MB of external data memory
 - External accesses supported at up to 60 MHz (zero wait states)

56F8357 Peripheral Circuit Features

- > Two PWM modules with 12 outputs and seven programmable fault inputs
- > Two serial peripheral interfaces (SPIs)
- > Two serial communications interfaces (SCIs)
- > Sixteen 16-bit timers with input and output compare capability
- > Two four-input quadrature decoders
- > FlexCAN module, 2.0 A/B compatible
- > I²C communications master mode (emulated)
- > Temperature sense diode to monitor the on-chip temperature
- > On-chip 3.3V to 2.6V voltage regulator
- > Software-programmable Phase-Lock Loop (PLL)
- > On-chip relaxation oscillator
- > 12-bit ADCs with 16 inputs, self-calibration and current injection capability
- > Up to 76 GPIO pins
- > External reset input pin for hardware reset
- > Computer operating properly (COP)
- > Integrated power-on reset and LVI module

Product Documentation

56F8300 Peripheral User Manual	Detailed peripheral descriptions of the 56F8300 series of devices Order Number: MC56F8300UM
56F8357/56F8157 Technical Data Sheet	Electrical and timing specifications, pin descriptions and package descriptions Order Number: MC56F8357
56F8357 Product Brief	Summary description and block diagram of the 56F8357 core, memory, peripherals and interfaces Order Number: MC56F8357PB
DSP56800E Reference Manual	Detailed description of the DSP56800E architecture, 16-bit core processor and the instruction set Order Number: DSP56800ERM

Award-Winning Development Environment

- > Processor Expert™ (PE) technology provides a rapid application design (RAD) tool that combines easy-to-use, component-based software application creation with an expert knowledge system.
- > The CodeWarrior™ Integrated Development Environment (IDE) is a sophisticated tool for code navigation, compiling and debugging. A comprehensive set of evaluation modules (EVMs) and development system cards will support concurrent engineering. Together, PE technology, the CodeWarrior tool suite and EVMs create a comprehensive, scalable tools solution for easy, fast and efficient development.

Ordering Information

Part	MC56F8357
Package Type	Low-Profile Quad Flat Pack (LQFP)
Pin Count	160
Temperature Range	-40°C to +105°C
Order Number	MC56F8357VPY60
<hr/>	
Part	MC56F8357
Package Type	Low-Profile Quad Flat Pack (LQFP)
Pin Count	160
Temperature Range	-40°C to +125°C
Order Number	MC56F8357MPY60

Learn More: For more information about Freescale products, please visit www.freescale.com.