wafer level chip-scale package; 6 bumps; $0.69 \times 1.09 \times 0.38$ mm (Backside coating included)

8 February 2016

Package information

1. Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP

Package type industry code WLCSP6

Package style descriptive codeUC (uncased chip)Package style suffix codeNA (not applicable)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 19-11-2014

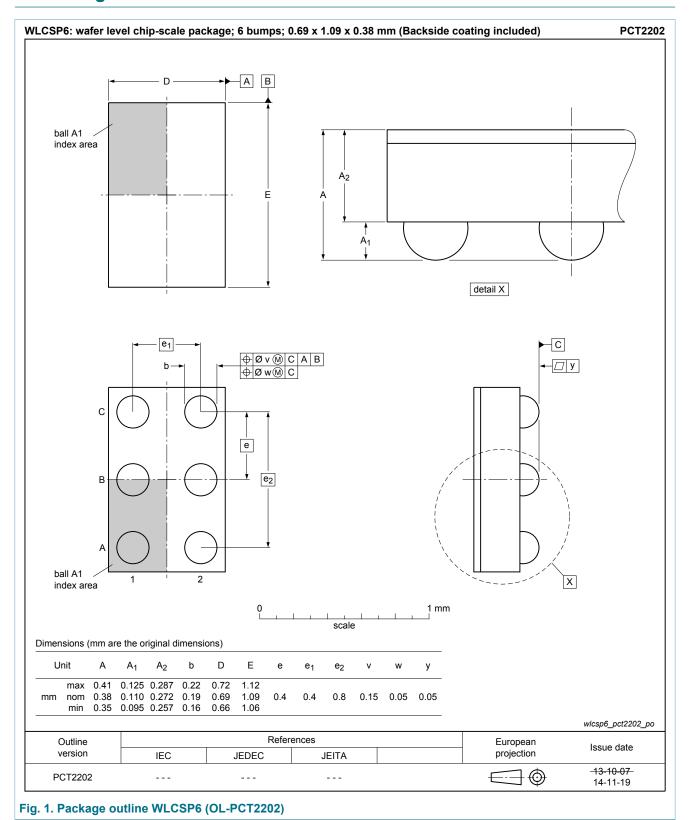
Table 1. Package summary

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	0.66	-	0.69	0.72	mm
E	package width	1.06	-	1.09	1.12	mm
Α	seated height	0.35	-	0.38	0.41	mm
е	nominal pitch	-	-	0.4	-	mm
n ₂	actual quantity of termination	-	-	6	-	



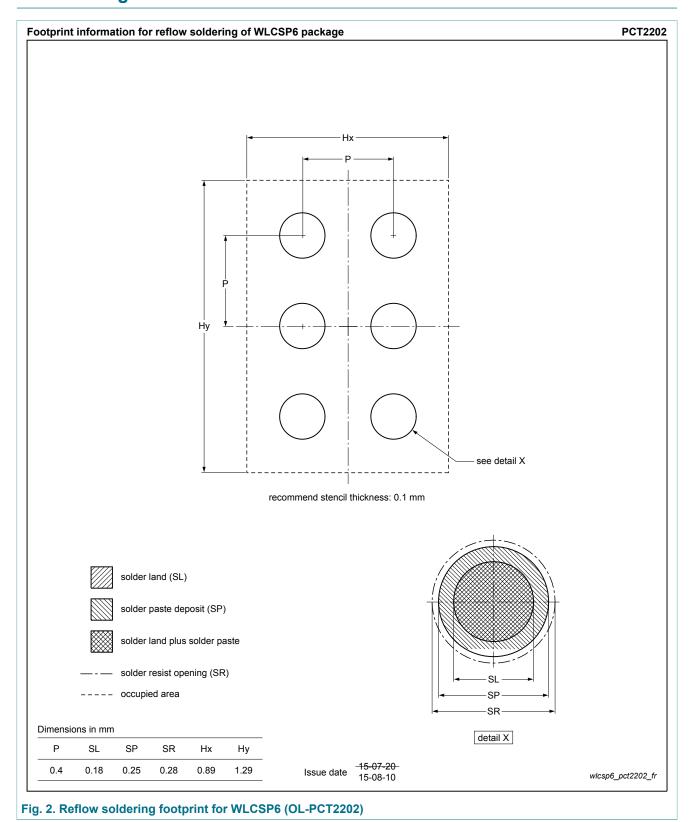
wafer level chip-scale package; 6 bumps; 0.69 x 1.09 x 0.38 mm (Backside coating included)

2. Package outline



wafer level chip-scale package; 6 bumps; 0.69 x 1.09 x 0.38 mm (Backside coating included)

3. Soldering



wafer level chip-scale package; 6 bumps; 0.69 x 1.09 x 0.38 mm (Backside coating included)

4. Legal information

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wafer level chip-scale package; 6 bumps; 0.69 x 1.09 x 0.38 mm (Backside coating included)

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